



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

19C

5/14/03

In re the Application of:

Confirmation No.: 6342

TANIGUCHI, et al

Art Unit: 2653

Serial No.: 09/266,869

Examiner: Gautam R. Patel

Filed: March 12, 1999

Docket No.: 108075-09008

For: MICROCONTROLLER HAVING PREFETCH FUNCTION

RECEIVED

MAY 13 2003

Technology Center 2600

May 9, 2003

PRELIMINARY AMENDMENT

Commissioner for Patents
Washington, D.C. 20231

Sir:

Prior to initial examination of the application, please amend the above-identified application as follows:

IN THE CLAIMS:

Please amend claims 1, 3, 8, 9, 14 and 24-34 as follows:

1. (Thrice Amended) A method for prefetching instructions and data of a program stored in a memory, wherein the program includes a pseudo instruction and at least one instruction, the pseudo instruction being arranged before the at least one instruction and including an address for the at least one instruction or an address for data, the method comprising the steps of:

reading the program from the memory;

detecting the pseudo instruction with a first unit;